IO Types Notation

Signal	Description	Signal	Description
IN	Digital CMOS input	**NDY**	NVDD_DR_YICE
OUT	Digital CMOS output	**AOUT**	Analog Output
IO	Digital CMOS input / output	**AIO**	Anolog Input/Output
ANG	Analog	**AIN**	Analog Input
3V3		**NC**	Not Connected
GND	Ground		
PWR	Power supply		

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
1	PWR	VIN		PWR	VIN		PWR	VIN			Power	VIN				Module power supply input (3.0V-5.5V)
2	PWR	VIN		PWR	VIN		PWR	VIN			Power	VIN				
3	PWR	VIN		PWR	VIN		PWR	VIN			Power	VIN				
4	PWR	VIN		PWR	VIN		PWR	VIN			Power	VIN				
5	PWR	VOUT		PWR	VOUT		PWR	VIO	PWR_SW2		Power	VIO				3.3V power supply output (up to 0.2A)
6	PWR	VOUT		PWR	VOUT		PWR	VIO	PWR_SW2		Power	VIO				
7	PWR	VOUT		PWR	VOUT		PWR	VIO	PWR_SW2		Power	VIO				
8	3V3	BOOTMODE		3V3	BOOT_MODE		1V8	BOOT MODE	IMX_BOOT_MODE	src_BOOT_MODE[0] src_BOOT_MODE[1]	1V8	BOOT MODE	IMX_BOOT_MODE			Boot mode select H: Boot from NAND / L: Boot from UART/USB
9	PWR	VOUT		PWR	VOUT		PWR	VOUT	PWR_SW1		Power	3V3				3.3V power supply output (up to 0.2A)
10	PWR	VOUT		PWR	VOUT		PWR	VOUT	PWR_SW1		Power	3V3				
11	PWR	VOUT		PWR	VOUT		PWR	VOUT	PWR_SW1		Power	3V3				
12	PWR	VOUT		PWR	VOUT		PWR	VOUT	PWR_SW1		Power	3V3				
13	PWR		BAT_VDD	PWR	VBACKUP	VBACKUP	2V5 to 3V3	VBACKUP	PMIC_LICELL		Power	VBACKUP				DRYICE backup power supply input (max. 1.55V)
14	NC				POWER_ON	POWER_ON	UP to VIN	POWER_ON	PMIC_PWRON1		VIN	POWER_ON			100K-PU to VIN	Not Connected
15	3V3		VSTBY_ACK	3V3	#RESET_OUT	VSTBY_ACK	1V8	RESET_OUT	IMX_DI1_PIN13	gpio3_GPIO[2]	3V3	#RESET_OUT	GPIO_17	ESAI1_TX0 SDMA_EXT_EVENT[0] GPC_PMIC_RDY RTC_CE_RTC_FSV_TRI	GPIO7[12]	“Pulse” indication on finish of internal system reset, by visibility of “hreset_b“ signal. After

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
														G SPDIF_OUT1 SNOOP2 SJC_JTAG_ACT		reset, this pin can be used for other purposes.
16	3V3	#POR		3V3	#POR		UP to VIN	POR_IN	IMX_POR_B	src_POR_B	VIN	#POR	POR_B		10K-PU to VIN	Power On Reset - active low input signal. Typically a push button reset. Pull low to force a reset. Leave unconnected or connect to 3V3 if unused. 63.5kΩ pull-up resistor.
17	3V3	RESET_IN_B	RESET_IN_B	3V3	RESET_IN_B	RESET_IN_B	1V8	RESET_IN	IMX_RESET_IN_B PMIC_RESETB	src_RESET_B	1V8	#RESET_IN	RESET_IN_B	RESET_B	10K-PU to 1V8	Master Reset - external active low Schmitt trigger input signal. When this signal goes active, all modules (except the reset module, SDRAMC module, and the clock control module) are reset.
18	GND	GND		GND	GND		GND	GND	GND		GND	GND				
19	ANG	ETN_TXN		ANG	ETN_TXN		ANG	PHY_TXN	LAN_TXN		Analog	ETN_TXN				Transmit Data Negative: 100Base-TX or 10Base-T differential transmit output to magnetics.
20	3V3	#ETN_LINKLED		3V3	#ETN_LINKLED		3V3	PHY_LINK	LAN_LINK		3V3	ETN_LINK_LED				Active low LINK ON indication: Active indicates that the link is on.
21	ANG	ETN_TXP		ANG	ETN_TXP		ANG	PHY_TXP	LAN_TXP		Analog	ETN_TXP				Transmit Data Positive: 100Base-TX or 10Base-T differential transmit output to magnetics.
22	PWR	ETN_3V3		PWR	ETN_3V3		PWR	PHY_VDD	LAN_VDD33		Power	ETN_VDD				+3.3V analog power supply output to magnetics
23	ANG	ETN_RXN		ANG	ETN_RXN		ANG	PHY_RXN	LAN_RXP		Analog	ETN_RXN				Receive Data Negative: 100Base-TX or 10Base-T differential receive input from magnetics.
24	3V3	#ETN_ACTLED		3V3	#ETN_ACTLED		3V3	PHY_ACT	LAN_ACTIVITY		3V3	ETN_ACT_LED				Active low ACTIVITY indication: Active indicates that there is Carrier sense (CRS) from the active PMD.
25	ANG	ETN_RXP		ANG	ETN_RXP		ANG	PHY_RXP	LAN_RXN		Analog	ETN_RXP				Receive Data Positive: 100Base-TX or 10Base-T differential receive input from magnetics.
26	GND	GND		GND	GND		GND	GND	GND		GND	GND				
27	3V3	USBH_VBUSEN	D9	3V3	USBH_VBUSEN	D9	3V3	USB_VBUS_EN	USB_CPEN		3V3	USBH_VBUS_EN	EIM_D31	WEIM_D[31] UART3_RTS CSI0_D[2] DI0_PIN12 DISP1_DAT[20] USBOH3_USBH1_PWR USBOH3_USBH2_PWR	GPIO3[31]	Active high external 5V supply enable. This pin is used to enable the external VBUS power supply.
28	3V3	#USBH_OC	D8	3V3	#USBH_OC	D8	1V8	USBH_OC	IMX_GPIO1_6	gpio1_GPIO[6] ccm_SSI_EXT2_CLK epit2_EPITO gpt_CAPIN2	3V3	#USBH_OC	EIM_D30	WEIM_D[30] UART3_CTS CSI0_D[3] DI0_PIN11 DISP1_DAT[21] USBOH3_USBH1_OC USBOH3_USBH2_OC	GPIO3[30] 10K-PU	Active low over-current indicator input connected to a GPIO. This signal can be used as an input only. 10kΩ pull-up resistor.
29	ANG	USBH_DM	USBPHY2_DM	DATA	USBH_DM	USBPHY2_DM	ANG	USBH_DM	USB_DM		Analog	USBH_DM	USB_H1_DN			D- pin of the USB cable

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30	NC			NC			5V	USBH_VBUS	USB_VBUS		Analog	USBH_VBUS	USB_H1_VBUS			Not Connected
31	ANG	USBH_DP	USBPHY2_DP	DATA	USBH_DP	USBPHY2_DP	ANG	USBH_DP	USB_DP		Analog	USBH_DP	USB_H1_DP			D+ pin of the USB cable
32	GND	GND		GND	GND		GND	GND	GND		GND	GND				
33	3V3	USBOTG_ID	USBPHY1_UID	3V3	USBOTG_ID	USBPHY1_UID	ANG	USBO_ID	IMX_ID		3V3	USBO_ID	USB_OTG_ID			ID pin of the USB cable. For an A-Device ID is grounded. For a BDevice ID is floated.
34	3V3	USBOTG_VBUSEN	GPIO_A	3V3	USBOTG_VBUSEN	GPIO_A	1V8	USBO_VBUS_EN	IMX_GPIO1_8	gpio1_GPIO[8] usboh3_USB_PWR ccm_CLKO2 esdhc2_CD	3V3	USBO_VBUS_EN CAN_TX	GPIO_7	ESAI1_TX4_RX1 EPIT1_EPITO CAN1_TXCAN UART2_TXD FIRI_RXD SPDIF_PLOCK CCM_PLL2_BYP	**GPIO1[7]**	Active high external 5V supply enable. This pin is used to enable the external VBUS power supply.
35	ANG	USBOTG_DM	USBPHY1_DP	DATA	USBOTG_DM	USBPHY1_DP	ANG	USBO_ DM	IMX_DN		Analog	USBO_DM	USB_OTG_DN			D- pin of the USB cable
36	3V3	#USBOTG_OC	GPIO_B	3V3	#USBOTG_OC	GPIO_B	1V8	USBO_OC	IMX_GPIO1_9	gpio1_GPIO[9] usboh3_USB_OC ccm_CLKO esdhc2_LCTL	3V3	#USBO_OC CAN_RX	GPIO_8	ESAI1_TX5_RX0 EPIT2_EPITO CAN1_RXCAN UART2_RXD FIRI_TXD SPDIF_SRCLK CCM_PLL3_BYP	**GPIO1[8]** 10K-PU	Active low over-current indicator input connected to a GPIO. 10kΩ pull-up resistor.
37	ANG	USBOTG_DP	USBPHY1_DP	DATA	USBOTG_DP	USBPHY1_DP	ANG	USBO_DP	IMX_DP		Analog	USBO_DP	USB_OTG_DP			D+ pin of the USB cable
38	ANG	USBOTG_VBUS	USBPHY1_VBUS	ANG	USBOTG_VBUS	USBPHY1_VBUS	5V	USBO_VBUS	IMX_VBUS		Analog	USBO_VBUS	USB_OTG_VBUS			VBUS pin of the USB cable. This pin is used for the VBUS comparator inputs.
39	GND		GND	GND		GND	GND	GND	GND		GND	GND				
40	3V3	I2C_DATA	I2C1_DAT	3V3	I2C_DATA	I2C1_DAT	1V8	I2C DATA	IMX_EIM_D16	gpio2_GPIO[0] uart2_CTS i2c1_SDA audmux_AUD4_RXFS audmux_AUD5_TXD	3V3	I2C_DATA	GPIO_6	ESAI1_SCKT **I2C3_SDA** CCM_CCM_OUT_0 CSU_INT_DEB OBSRV_INT_OUT1 ESDHC2_LCTL MLBSIG	GPIO1[6] 10K-PU	I2C Data
41	3V3	I2C_CLK	I2C1_CLK	3V3	I2C_CLK	I2C1_CLK	1V8	I2C CLK	IMX_EIM_D19	gpio1_GPIO[2] uart2_RTS i2c1_SCL audmux_AUD4_RXC audmux_AUD5_TXFS	3V3	I2C_CLK	GPIO_3	ESAI1_HCKR **I2C3_SCL** DPLLIP1_TOG_EN CCM_CLKO2 OBSRV_INT_OUT0 USBOH3_USBH1_OC MLBCLK	GPIO1[3] 10K-PU	I2C Clock
42	3V3	PWM	PWM	3V3	PWM	PWM	1V8	PWM	IMX_GPIO1_2	gpio1_GPIO[2] pwm1_PWMO i2c2_SCL	3V3	PWM	GPIO_1	ESAI1_SCKR KPP_ROW[5] CCM_SSI_EXT2_CLK **PWM2_PWMO** WDOG2_WDOG_B ESDHC1_CD TESTER_ACK	GPIO1[1]	PWM Output
43	3V3	OWDAT	RTCK	3V3	OWDAT	RTCK	1V8	O WIRE	IMX_OWIRE_LINE	owire_LINE gpio1_GPIO[24] spdif_OUT1 src_SYSTEM_RST	3V3	OWIRE	GPIO_18	ESAI1_TX1 SDMA_EXT_EVENT[1] **OWIRE_LINE** RTC_CE_RTC_ALARM2_TRIG CCM_ASRC_EXT_CLK ESDHC1_LCTL SYSTEM_RST	GPIO7[13]	1-Wire bus. Requires an external pull-up resistor. The recommended resistor is specified by the generic 1-Wire device used in a given system.
44	3V3	CSPI_SS0	CSPI1_SS0	3V3	CSPI_SS0	CSPI1_SS0	1V8	CSPI_SS0	IMX_CSPI1_SS0	ecspi1_SS0 audmux_AUD4_TXC gpio4_GPIO[24]	3V3	CSPI_SS0	EIM_EB2	WEIM_EB[2] CCM_DI1_EXT_CLK SER_DISP1_CS **ECSPI1_SS0** I2C2_SCL	GPIO2[30]	Slave Select (Selectable polarity) signal

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
										ecspi1_SS1				WEIM_D[19] DI0_PIN8		
										~~audmux_AUD4_TXD~~				~~DISPB1_SER_RS~~		
45	3V3	CSPI_SS1	CSPI1_SS1	3V3	CSPI_SS1	CSPI1_SS1	1V8	CSPI_SS1	IMX_CSPI1_SS1	gpio4_GPIO[25]	3V3	CSPI_SS1	EIM_D19	**ECSPI1_SS1** EPIT1_EPITO UART1_CTS USBOH3_USBH2_OC	GPIO3[19]	Slave Select (Selectable polarity) signal
46	3V3	CSPI_MOSI	CSPI1_MOSI	3V3	CSPI_MOSI	CSPI1_MOSI	1V8	CSPI_MOSI	IMX_CSPI1_MOSI	ecspi1_MOSI i2c1_SDA gpio4_GPIO[22]	3V3	CSPI_MOSI	EIM_D18	WEIM_D[18] DI0_PIN7 DISPB1_SER_DIO **ECSPI1_MOSI** I2C3_SDA DI1_D0_CS	GPIO3[18]	Master Out/Slave In signal
47	3V3	CSPI_MISO	CSPI1_MISO	3V3	CSPI_MISO	CSPI1_MISO	1V8	CSPI_MISO	IMX_CSPI1_MISO	ecspi1_MISO audmux_AUD4_RXD gpio4_GPIO[23]	3V3	CSPI_MISO	EIM_D17	ecspi1_MISOWEIM_D[17] DI0_PIN6 DISPB1_SER_DIN **ECSPI1_MISO** I2C3_SCL	GPIO3[17]	Master In/Slave Out signal
48	3V3	CSPI_SCLK	CSPI1_SCLK	3V3	CSPI_SCLK	CSPI1_SCLK	1V8	CSP_CLK	IMX_CSPI1_SCLK	ecspi1_SCLK i2c1_SCL gpio4_GPIO[27]	3V3	CSP_CLK	EIM_D16	WEIM_D[16] DI0_PIN5 DISPB1_SER_CLK **ECSPI1_SCLK** I2C2_SDA	GPIO3[16]	Serial Clock signal
49	3V3	CSPI_RDY	CSPI1_RDY	3V3	CSPI_RDY	CSPI1_RDY	1V8	CSPI_RDY	IMX_CSPI1_RDY	ecspi1_RDY audmux_AUD4_TXFS gpio4_GPIO[26]	3V3	CSPI_RDY	GPIO_19	KPP_COL[5] CCM_CLKO SPDIF_OUT1 RTC_CE_RTC_EXT_TRIG2 **ECSPI1_RDY** FEC_TDATA[3] INT_BOOT	GPIO4[5]	Serial Data Ready signal
50	GND	GND		GND	GND		GND	GND	GND		GND	GND				
51	3V3	SD1_CD	BCLK	3V3	SD1_CD	BCLK	1V8	SD1_CD	IMX_NANDF_D12	gpio3_GPIO[28]	3V3	SD1_CD	EIM_D24	WEIM_D[24] UART3_TXD ECSPI1_SS2 CSPI_SS2 AUD5_RXFS ECSPI2_SS2 UART1_DTR	**GPIO3[24]**	SD Card Detect – connected to a GPIO
52	3V3	SD1_D[0]	SD1_DATA0	3V3	SD1_D[0]	SD1_DATA0	1V8	SD1_D0	IMX_SD1_DATA0	esdhc1_DAT0 audmux_AUD5_TXD cspi_MISO	3V3	SD1_D0	SD1_DATA0	**ESDHC1_DAT0** GPT_CAPIN1 CSPI_MISO CCM_PLL3_BYP	GPIO1[16]	SD Data bidirectional signals—If the system designer does not want to make use of the internal pull-up, via the Pull-up enable register, a 50 K–69 K external pull up resistor must be added.
53	3V3	SD1_D[1]	SD1_DATA1	3V3	SD1_D[1]	SD1_DATA1	1V8	SD1_D1	IMX_SD1_DATA1	esdhc1_DAT1 audmux_AUD5_RXD	3V3	SD1_D1	SD1_DATA1	**ESDHC1_DAT1** GPT_CAPIN2 CSPI_SS0 CCM_PLL4_BYP	GPIO1[17]	
54	3V3	SD1_D[2]	SD1_DATA2	3V3	SD1_D[2]	SD1_DATA2	1V8	SD1_D2	IMX_SD1_DATA2	esdhc1_DAT2 audmux_AUD5_TXC	3V3	SD1_D2	SD1_DATA2	**ESDHC1_DAT2** GPT_CMPOUT2 PWM2_PWMO WDOG1_WDOG_B CSPI_SS1 CCM_PLL2_BYP	GPIO1[19]	
55	3V3	SD1_D[3]	SD1_DATA3	3V3	SD1_D[3]	SD1_DATA3	1V8	SD1_D3	IMX_SD1_DATA3	esdhc1_DAT3 audmux_AUD5_TXFS cspi_SS1	3V3	SD1_D3	SD1_DATA3	**ESDHC1_DAT3** GPT_CMPOUT3 PWM1_PWMO WDOG2_WDOG_B CSPI_SS2 SATA_PHY_DTB[1]	GPIO1[21]	
56	3V3	SD1_CMD	SD1_CMD	3V3	SD1_CMD	SD1_CMD	1V8	SD1_CMD	IMX_SD1_CMD	esdhc1_CMD audmux_AUD5_RXFS cspi_MOSI	3V3	SD1_CMD	SD1_CMD	**ESDHC1_CMD** GPT_CMPOUT1 CSPI_MOSI CCM_PLL1_BYP	GPIO1[18]	SD Command bidirectional signal
	3V3	SD1_CLK	SD1_CLK	3V3	SD1_CLK	SD1_CLK	1V8	SD1_CLK	IMX_SD1_CLK	esdhc1_CLK	3V3	SD1_CLK	SD1_CLK	**ESDHC1_CLK**	GPIO1[20]	SD Output Clock

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
57										audmux_AUD5_RXC cspi_SCLK				OSC32K_32K_OUT GPT_CLKIN CSPI_SCLK SATA_PHY_DTB[0]		
58	GND	GND		GND	GND		GND	GND	GND		GND	GND				
59	3V3	UART1_TXD	UART1_TXD	3V3	UART1_TXD	UART1_TXD	1V8	UART1_TXD	IMX_UART1_TXD	uart1_TXD_MUX pwm2_PWMO gpio4_GPIO[29]	3V3	UART1_TXD	PATA_DIOW	PATA_DIOW **UART1_TXD** USBPHY2_DATAOUT[2]	GPIO6[17]	Transmit Data output signal
60	3V3	UART1_RXD	UART1_RXD	3V3	UART1_RXD	UART1_RXD	1V8	UART1_RXD	IMX_UART1_RXD	uart1_RXD_MUX gpio4_GPIO[28]	3V3	UART1_RXD	PATA_DMACK	PATA_DMACK **UART1_RXD** USBPHY2_DATAOUT[3]	GPIO6[18]	Receive Data input signal
61	3V3	UART1_RTS	UART1_RTS	3V3	UART1_RTS	UART1_RTS	1V8	UART1_RTS	IMX_UART1_RTS	uart1_RTS gpio4_GPIO[30]	3V3	UART1_RTS	PATA_IORDY	PATA_IORDY ESDHC3_CLK **UART1_RTS** CAN2_RXCAN USBPHY1_DATAOUT[1]	GPIO7[5]	Request to Send input signal
62	3V3	UART1_CTS	UART1_CTS	3V3	UART1_CTS	UART1_CTS	1V8	UART1_CTS	IMX_UART1_CTS	uart1_CTS gpio4_GPIO[31]	3V3	UART1_RTS	PATA_IORDY	PATA_IORDY ESDHC3_CLK **UART1_RTS** CAN2_RXCAN USBPHY1_DATAOUT[1]	GPIO7[5]	Clear to Send output signal
63	3V3	UART2_TXD	UART2_TXD	3V3	UART2_TXD	UART2_TXD	1V8	UART2_TXD	IMX_UART2_TXD	uart2_TXD_MUX firi_RXD gpio1_GPIO[21]	3V3	UART2_TXD	PATA_DMARQ	PATA_DMARQ **UART2_TXD** CCM_CCM_OUT_0 USBPHY2_DATAOUT[4]	GPIO7[0]	Transmit Data output signal
64	3V3	UART2_RXD	UART2_RXD	3V3	UART2_RXD	UART2_RXD	1V8	UART2_RXD	IMX_UART2_RXD	uart2_RXD_MUX firi_TXD gpio1_GPIO[20]	3V3	UART2_RXD	PATA_BUFFER_EN	PATA_BUFFER_EN **UART2_RXD** CCM_CCM_OUT_1 USBPHY2_DATAOUT[5]	GPIO7[1]	Receive Data input signal
65	3V3	UART2_RTS	UART2_RTS	3V3	UART2_RTS	UART2_RTS	1V8	UART2_RTS	IMX_EIM_D26	kpp_COL[7] uart3_TXD_MUX uart2_RTS gpt_CMPOUT2	3V3	UART2_RTS	PATA_DIOR	PATA_DIOR **UART2_RTS** CAN1_RXCAN USBPHY2_DATAOUT[7]	GPIO7[3]	Request to Send input signal
66	3V3	UART2_CTS	UART2_CTS	3V3	UART2_CTS	UART2_CTS	1V8	UART2_CTS	IMX_EIM_D25	uart1_DSR uart3_RXD_MUX uart2_CTS gpt_CMPOUT1	3V3	UART2_CTS	PATA_INTRQ	PATA_INTRQ **UART2_CTS** CAN1_TXCAN CCM_CCM_OUT_2 USBPHY2_DATAOUT[6]	GPIO7[2]	Clear to Send output signal
67	3V3	UART5_TXD	ECB	3V3	UART3_TXD	UART3_TXD	1V8	UART3_TXD	IMX_UART3_TXD	uart1_DSR uart3_TXD_MUX gpio1_GPIO[23]	3V3	UART3_TXD	PATA_CS_0	PATA_CS_0 **UART3_TXD** USBPHY1_DATAOUT[5]	GPIO7[9]	Transmit Data output signal
68	3V3	UART5_RXD	LBA	3V3	UART3_RXD	UART3_RXD	1V8	UART3_RXD	IMX_UART3_RXD	uart1_DTR uart3_RXD_MUX gpio1_GPIO[22]	3V3	UART3_RXD	PATA_CS_1	PATA_CS_1 **UART3_RXD** USBPHY1_DATAOUT[6]	GPIO7[10]	Receive Data input signal
69	3V3	UART5_RTS	CS5	3V3	UART3_RTS	UART3_RTS	1V8	UART3_RTS	IMX_EIM_D18	gpio2_GPIO[2] uart2_TXD_MUX uart3_RTS audmux_AUD5_TXC	3V3	UART3_RTS	PATA_DA_2	PATA_DA_2 ESDHC4_CLK **UART3_RTS** USBPHY1_DATAOUT[4]	GPIO7[8]	Request to Send input signal
70	3V3	UART5_CTS	CS4	3V3	UART3_CTS	UART3_CTS	1V8	UART3_CTS	IMX_EIM_D17	gpio2_GPIO[1] uart2_RXD+MUX uart3_CTS audmux_AUD5_RXD	3V3	UART3_CTS	PATA_DA_1	PATA_DA_1 ESDHC4_CMD **UART3_CTS** USBPHY1_DATAOUT[3]	GPIO7[7]	Clear to Send output signal
71	GND	GND		GND	GND		GND	GND	GND		GND	GND				
72	3V3	KP_COL[0]	3V3	3V3	KP_COL[0]	KPP_COL0	1V8	KP_COL0	IMX_KEY_COL	kpp_COL[0]	3V3	KP_COL0	GPIO_9	ESAI1_FSR	GPIO1[9]	Keypad Column selection

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
									0					**KPP_COL[6]** CCM_REF_EN_B PWM1_PWMO WDOG1_WDOG_B ESDHC1_WP SCC_FAIL_STATE		signals.
73	3V3	KP_COL[1]	KPP_COL1	3V3	KP_COL[1]	KPP_COL1	1V8	KP_COL1	IMX_KEY_COL1	kpp_COL[1]	3V3	KP_COL1	GPIO_4	ESAI1_HCKT **KPP_COL[7]** CCM_CCM_OUT_2 CSU_ALARM_AUT[1] OBSRV_INT_OUT3 ESDHC2_CD SCC_SEC_STATE	GPIO1[4]	
74	3V3	KP_COL[2]	KPP_COL2	3V3	KP_COL[2]	KPP_COL2	1V8	KP_COL2	IMX_KEY_COL2	kpp_COL[2]	3V3	KP_COL2	KEY_COL2	**KPP_COL[2]** CAN1_TXCAN 32K_256K_CTI_TRIGOUT6 FEC_MDIO ECSPI1_SS1 FEC_RDATA[2] USBPHY1_RXACTIVE	GPIO4[10]	
75	3V3	KP_COL[3]	KPP_COL3	3V3	KP_COL[3]	KPP_COL3	1V8	KP_COL3	IMX_KEY_COL3	kpp_COL[3] src_INT_BOOT	3V3	KP_COL3	KEY_COL3	**KPP_COL[3]** USBOH3_H2_DP SPDIF_IN1 I2C2_SCL ECSPI1_SS3 FEC_CRS USBPHY1_SIECLOCK	GPIO4[12]	
76	3V3	TXCAN	GPIO_C	3V3	TXCAN	KPP_COL4	1V8	KP_COL4	IMX_KEY_COL4	kpp_COL[4] uart1_RI uart3_RTS i2c2_SCL spdif_OUT1	3V3	KP_COL4 TXCAN	KEY_COL4	KPP_COL[4] **CAN2_TXCAN** SISG[4] UART5_RTS USBOH3_USBOTG_OC USBPHY1_LINESTATE[1]	GPIO4[14]	Module specific function
77	3V3	KP_ROW[0]	KPP_ROW0	3V3	KP_ROW[0]	KPP_ROW0	1V8	KP_ROW0	IMX_KEY_ROW0	kpp_ROW[0]	3V3	KP_ROW0	GPIO_2	ESAI1_FST **KPP_ROW[6]** CCM_CCM_OUT_1 CSU_ALARM_AUT[0] OBSRV_INT_OUT2 ESDHC2_WP MLBDAT	GPIO1[2]	Keypad Row selection signals.
78	3V3	KP_ROW[1]	KPP_ROW1	3V3	KP_ROW[1]	KPP_ROW1	1V8	KP_ROW1	IMX_KEY_ROW1	kpp_ROW[1]	3V3	KP_ROW1	GPIO_5	ESAI1_TX2_RX3 **KPP_ROW[7]** CCM_CLKO CSU_ALARM_AUT[2] OBSRV_INT_OUT4 I2C3_SCL CCM_PLL1_BYP	GPIO1[5]	
79	3V3	KP_ROW[2]	KPP_ROW2	3V3	KP_ROW[2]	KPP_ROW2	1V8	KP_ROW2	IMX_KEY_ROW2	kpp_ROW[2]	3V3	KP_ROW2	KEY_ROW2	**KPP_ROW[2]** CAN1_RXCAN 32K_256K_CTI_TRIGOUT7 FEC_MDC ECSPI1_SS2 FEC_TDATA[2] USBPHY1_RXERROR	GPIO4[11]	
80	3V3	KP_ROW[3]	KPP_ROW3	3V3	KP_ROW[3]	KPP_ROW3	1V8	KP_ROW3	IMX_KEY_ROW3	kpp_ROW[3]	3V3	KP_ROW3	KEY_ROW3	**KPP_ROW[3]** USBOH3_H2_DM CCM_ASRC_EXT_CLK I2C2_SDA OSC32K_32K_OUT CCM_PLL4_BYP USBPHY1_LINESTATE[0]	GPIO4[13]	
81	3V3	RXCAN	GPIO_D	3V3	RXCAN	KPP_ROW4	1V8	KP_COL5	IMX_KEY_COL5	kpp_COL[5] uart1_DCD uart3_CTS i2c2_SDA	3V3	KP_ROW4 RXCAN	KEY_ROW4	KPP_ROW[4] **CAN2_RXCAN** SISG[5] UART5_CTS USBOH3_USBOTG_PWR USBPHY1_VBUSVALID	GPIO4[15]	Module specific function
82	GND	GND		GND	GND		GND	GND	GND		GND	GND				
83	3V3	SSI1_INT	EXT_ARMCL	3V3	SSI1_INT	SSI1_INT	1V8	SSI1_INT	IMX_DI1_D0_C	gpio3_GPIO[3]	3V3	SSI1_INT	EIM_D26	WEIM_D[26]	**GPIO3[26]**	GPIO

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
			K						S					UART2_TXD FIRI_RXD CSI0_D[1] DI1_PIN11 SISG[2] DISP1_DAT[22]		
84	3V3	SSI1_RXD	EB1	3V3	SSI1_RXD	AUD4_RXD	1V8	SSI1_RXD	IMX_AUD3_BB_RXD	audmux_AUD3_RXD uart3_RXD_MUX gpio4_GPIO[19]	3V3	SSI1_RXD	KEY_ROW1	KPP_ROW[1] **AUD5_RXD** 32K_256K_CTI_TRIGOUT_ACK7 UART5_RXD ECSPI1_SS0 FEC_COL USBPHY1_RXVALID	GPIO4[9]	Receive serial data
85	3V3	SSI1_TXD	EB0	3V3	SSI1_TXD	AUD4_TXD	1V8	SSI1_TXD	IMX_AUD3_BB_TXD	audmux_AUD3_TXD gpio4_GPIO[18]	3V3	SSI1_TXD	KEY_ROW0	KPP_ROW[0] **AUD5_TXD** 32K_256K_CTI_TRIGIN_ACK7 UART4_RXD ECSPI1_MOSI FEC_TX_ER	GPIO4[7]	Transmit serial data
86	3V3	SSI1_CLK	OE	3V3	SSI1_CLK	AUD4_TXC	1V8	SSI1_CLK	IMX_AUD3_BB_CK	audmux_AUD3_TXC gpio4_GPIO[20]	3V3	SSI1_CLK	KEY_COL0	KPP_COL[0] **AUD5_TXC** 32K_256K_CTI_TRIGIN7 UART4_TXD ECSPI1_SCLK FEC_RDATA[3] ANY_PU_RST	GPIO4[6]	Serial clock
87	3V3	SSI1_FS	RW	3V3	SSI1_FS	AUD4_TXFS	1V8	SSI1_FS	IMX_AUD3_BB_FS	audmux_AUD3_TXFS uart3_TXD_MUX gpio4_GPIO[21]	3V3	SSI1_FS	KEY_COL1	KPP_COL[1] **AUD5_TXFS** 32K_256K_CTI_TRIGOUT_ACK6 UART5_TXD ECSPI1_MISO FEC_RX_CLK USBPHY1_TXREADY	GPIO4[8]	Frame Sync
88	GND	GND		GND	GND		GND	GND	GND		GND	GND				
89	3V3	SSI2_INT	UPLL_BYPCLK	3V3	SSI2_INT	SSI2_INT	1V8	SSI2_INT	IMX_DI1_D1_CS	gpio3_GPIO[4]	3V3	SSI2_INT	EIM_D27	WEIM_D[27] UART2_RXD FIRI_TXD CSI0_D[0] DI1_PIN13 SISG[3] DISP1_DAT[23]	**GPIO3[27]**	GPIO
90	3V3	SSI2_RXD	POWER_FAIL	3V3	SSI2_RXD	AUD7_RXD	1V8	SSI2_RXD	IMX_EIM_D29	kpp_ROW[5] audmux_AUD6_RXD	3V3	SSI2_RXD	CSI0_DAT7	CSI0_D[7] KPP_ROW[6] ECSPI1_SS0 USBOH3_USBH3_DIR **AUD3_RXD**	GPIO5[25]	Receive serial data
91	3V3	SSI2_TXD	GPIO_E	3V3	SSI2_TXD	AUD7_TXD	1V8	SSI2_TXD	IMX_EIM_D28	kpp_ROW[4] audmux_AUD6_TXD	3V3	SSI2_TXD	CSI0_DAT5	CSI0_D[5] KPP_ROW[5] ECSPI1_MOSI USBOH3_USBH3_NXT **AUD3_TXD**	GPIO5[23]	Transmit serial data
92	3V3	SSI2_CLK	GPIO_F	3V3	SSI2_CLK	AUD7_TXC	1V8	SSI2_CLK	IMX_EIM_D30	kpp_ROW[6] audmux_AUD6_TXC	3V3	SSI2_CLK	CSI0_DAT4	CSI0_D[4] KPP_COL[5] ECSPI1_SCLK USBOH3_USBH3_STP **AUD3_TXC**	GPIO5[22]	Serial clock
93	3V3	SSI2_FS	VSTBY_REQ	3V3	SSI2_FS	AUD7_TXFS	1V8	SSI2_FS	IMX_EIM_D31	kpp_ROW[7] audmux_AUD6_TXFS	3V3	SSI2_FS	CSI0_DAT6	CSI0_D[6] KPP_COL[6] ECSPI1_MISO USBOH3_USBH3_CLK **AUD3_TXFS**	GPIO5[24]	Frame Sync
94	GND	GND		GND	GND		GND	GND	GND		GND	GND				
95	NC			NC			1V8	SD2_CD	IMX_NANDF_D13	gpio3_GPIO[27]	3V3	SD2_CD	EIM_D25	WEIM_D[25] UART3_RXD ECSPI1_SS3 CSPI_SS3 AUD5_RXC ECSPI2_SS3 UART1_DSR	GPIO3[25]	Not Connected

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
96	NC				SD2_D[0]	SD1_DATA0	1V8	SD2_D0	IMX_SD2_DATA0	esdhc2_DAT0 esdhc1_DAT4 cspi_MISO	3V3	SD2_D0	SD2_DATA0	**ESDHC2_DAT0** KPP_ROW[7] AUD4_RXD CSPI_MISO RTIC_RTIC_DONE_INT	GPIO1[15]	Not Connected
97	NC				SD2_D[1]	SD1_DATA1	1V8	SD2_D1	IMX_SD2_DATA1	esdhc2_DAT1 esdhc1_DAT5 usboh3_H2_DP	3V3	SD2_D1	SD2_DATA1	**ESDHC2_DAT1** KPP_COL[7] AUD4_TXFS CSPI_SS0 RTIC_RTIC_SEC_VIO	GPIO1[14]	Not Connected
98	NC				SD2_D[2]	SD1_DATA2	1V8	SD2_D2	IMX_SD2_DATA2	esdhc2_DAT2 esdhc1_DAT6 usboh3_H2_DM	3V3	SD2_D2	SD2_DATA2	**ESDHC2_DAT2** KPP_ROW[6] AUD4_TXD CSPI_SS1 SJC_FAIL	GPIO1[13]	Not Connected
99	NC				SD2_D[3]	SD1_DATA3	1V8	SD2_D3	IMX_SD2_DATA3	esdhc2_DAT3 esdhc1_DAT7 cspi_SS2	3V3	SD2_D3	SD2_DATA3	**ESDHC2_DAT3** KPP_COL[6] AUD4_TXC CSPI_SS2 SJC_DONE	GPIO1[12]	Not Connected
100	NC				SD2_CMD	SD1_CMD	1V8	SD2_CMD	IMX_SD2_CMD	esdhc2_CMD i2c1_SCL cspi_MOSI	3V3	SD2_CMD	SD2_CMD	**ESDHC2_CMD** KPP_ROW[5] AUD4_RXC CSPI_MOSI SCC_RANDOM	GPIO1[11]	Not Connected
101	NC				SD2_CLK	SD2_CLK	1V8	SD2 CLK	IMX_SD2_CLK	esdhc2_CLK i2c1_SDA cspi_SCLK	3V3	SD2 CLK	SD2_CLK	**ESDHC2_CLK** KPP_COL[5] AUD4_RXFS CSPI_SCLK SCC_RANDOM_V	GPIO1[10]	Not Connected
102	GND	GND		GND	GND		GND	GND	GND		GND	GND				
103	3V3	CSI_D0	CSI_D2	3V3	CSI_D0	CSI_D2	1V8	CSI_D0	IMX_CSI2_D12	CSI2_D[12] gpio4_GPIO[9]	3V3	CSI_D0	CSI0_DAT12	**CSI0_D[12]** UART4_TXD USBOH3_USBH3_DATA[0]	GPIO5[30]	Sensor port data
104	3V3	CSI_D1	CSI_D3	3V3	CSI_D1	CSI_D3	1V8	CSI_D1	IMX_CSI2_D13	CSI2_D[13] gpio4_GPIO[10]	3V3	CSI_D1	CSI0_DAT13	**CSI0_D[13]** UART4_RXD USBOH3_USBH3_DATA[1]	GPIO5[31]	Sensor port data
105	3V3	CSI_D2	CSI_D4	3V3	CSI_D2	CSI_D4	1V8	CSI_D2	IMX_CSI2_D14	CSI2_D[14]	3V3	CSI_D2	CSI0_DAT14	**CSI0_D[14]** UART5_TXD USBOH3_USBH3_DATA[2]	GPIO6[0]	Sensor port data
106	3V3	CSI_D3	CSI_D5	3V3	CSI_D3	CSI_D5	1V8	CSI_D3	IMX_CSI2_D15	CSI2_D[15]	3V3	CSI_D3	CSI0_DAT15	**CSI0_D[15]** UART5_RXD USBOH3_USBH3_DATA[3]	GPIO6[1]	Sensor port data
107	3V3	CSI_D4	CSI_D6	3V3	CSI_D4	CSI_D6	1V8	CSI_D4	IMX_CSI2_D16	CSI2_D[16]	3V3	CSI_D4	CSI0_DAT16	**CSI0_D[16]** UART4_RTS USBOH3_USBH3_DATA[4]	GPIO6[2]	Sensor port data
108	3V3	CSI_D5	CSI_D7	3V3	CSI_D5	CSI_D7	1V8	CSI_D5	IMX_CSI2_D17	CSI2_D[17]	3V3	CSI_D5	CSI0_DAT17	**CSI0_D[17]** UART4_CTS USBOH3_USBH3_DATA[5]	GPIO6[3]	Sensor port data
109	3V3	CSI_D6	CSI_D8	3V3	CSI_D6	CSI_D8	1V8	CSI_D6	IMX_CSI2_D18	CSI2_D[18] gpio4_GPIO[11]	3V3	CSI_D6	CSI0_DAT18	**CSI0_D[18]** UART5_RTS USBOH3_USBH3_DATA[6]	GPIO6[4]	Sensor port data
110	3V3	CSI_D7	CSI_D9	3V3	CSI_D7	CSI_D9	1V8	CSI_D7	IMX_CSI2_D19	CSI2_D[19] gpio4_GPIO[12]	3V3	CSI_D7	CSI0_DAT19	**CSI0_D[19]** UART5_CTS USBOH3_USBH3_DATA[7] USBPHY2_BISTOK	3V3	Sensor port data
111	GND	GND		GND	GND		GND	GND	GND		GND	GND				
112	3V3	CSI_HSYNC	CSI_HSYNC	3V3	CSI_HSYNC	CSI_HSYNC	1V8	CSI_HSYNC	IMX_CSI2_HSYNC	CSI2_VSYNC gpio4_GPIO[13]	3V3	CSI_HSYNC	CSI0_MCLK	**CSI0_HSYNC** CCM_CSI0_MCLK	GPIO5[19]	Sensor port horizontal sync
	3V3	CSI_VSYN	CSI_VSYNC	3V3	CSI_VSYNC	CSI_VSYNC	1V8	CSI_VSYNC	IMX_CSI2_VSY	CSI2_HSYNC	3V3	CSI_VSYNC	CSI0_VSYNC	**CSI0_VSYNC**	GPIO5[21]	Sensor port vertical sync

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
113		C							NC	gpio4_GPIO[14]						
114	3V3	CSI_PIXCLK	CSI_PIXCLK	3V3	CSI_PIXCLK	CSI_PIXCLK	1V8	CSI_PCLK	IMX_CSI2_PIXCLK	CSI2_PIXCLK gpio4_GPIO[15]	3V3	CSI_PIXCLK	CSI0_PIXCLK	**CSI0_PIXCLK**	GPIO5[18]	Sensor port data latch clock
115	3V3	CSI_MCLK	CSI_MCLK	3V3	CSI_MCLK	CSI_MCLK	1V8	CSI_MCLK	CSI_MCLK	gpio1_GPIO[5] wdog2_WDOG_B ccm_CLKO ccm_CSI2_MCLK	3V3	CSI_MCLK	GPIO_0	**CCM_CLKO** KPP_COL[5] CCM_SSI_EXT1_CLK EPIT1_EPITO SRTC_ALARM_DEB USBOH3_USBH1_PWRTD	GPIO1[0]	Sensor port master clock
116	GND	GND		GND	GND		GND	GND			GND	GND				
117	3V3	GPIO	CLKO	3V3	LCD_D0/GPIO	CLKO	1V8	LCD_D0	IMX_DISP1_DAT0	ipu_DISP1_DAT[0]	2V8	LCD_D0	DISP0_DAT0	**DISP0_DAT[0]** CSPI_SCLK USBOH3_USBH2_DATA[0] USBPHY2_TXREADY	GPIO4[21]	Clock out pin from CRM, clock source is controlable and can also be used for debug.
118	3V3	CONTRAST	CONTRAST	3V3	LCD_D1/CONTRAST	CONTRAST	1V8	LCD_D1	IMX_DISP1_DAT1	ipu_DISP1_DAT[1]	2V8	LCD_D1	DISP0_DAT1	**DISP0_DAT[1]** CSPI_MOSI USBOH3_USBH2_DATA[1] USBPHY2_RXVALID	GPIO4[22]	
119	3V3	LD0	LD0	3V3	LCD_D2	DISP1_DAT0	1V8	LCD_D2	IMX_DISP1_DAT2	ipu_DISP1_DAT[2]	2V8	LCD_D2	DISP0_DAT2	**DISP0_DAT[2]** CSPI_MISO USBOH3_USBH2_DATA[2] USBPHY2_RXACTIVE	GPIO4[23]	LCD Data Bus
120	3V3	LD1	LD1	3V3	LCD_D3	DISP1_DAT1	1V8	LCD_D3	IMX_DISP1_DAT3	ipu_DISP1_DAT[3]	2V8	LCD_D3	DISP0_DAT3	**DISP0_DAT[3]** CSPI_SS0 USBOH3_USBH2_DATA[3] USBPHY2_RXERROR	GPIO4[24]	LCD Data Bus
121	3V3	LD2	LD2	3V3	LCD_D4	DISP1_DAT2	1V8	LCD_D4	IMX_DISP1_DAT4	ipu_DISP1_DAT[4]	2V8	LCD_D4	DISP0_DAT4	**DISP0_DAT[4]** CSPI_SS1 USBOH3_USBH2_DATA[4] USBPHY2_SIECLOCK	GPIO4[25]	LCD Data Bus
122	3V3	LD3	LD3	3V3	LCD_D5	DISP1_DAT3	1V8	LCD_D5	IMX_DISP1_DAT5	ipu_DISP1_DAT[5]	2V8	LCD_D5	DISP0_DAT5	**DISP0_DAT[5]** CSPI_SS2 USBOH3_USBH2_DATA[5] USBPHY2_LINESTATE[0]	GPIO4[26]	LCD Data Bus
123	3V3	LD4	LD4	3V3	LCD_D6	DISP1_DAT4	1V8	LCD_D6	IMX_DISP1_DAT6	ipu_DISP1_DAT[6] src_BT_USB_SRC	2V8	LCD_D6	DISP0_DAT6	**DISP0_DAT[6]** CSPI_SS3 USBOH3_USBH2_DATA[6] USBPHY2_LINESTATE[1]	GPIO4[27]	LCD Data Bus
124	3V3	LD5	LD5	3V3	LCD_D7	DISP1_DAT5	1V8	LCD_D7	IMX_DISP1_DAT7	ipu_DISP1_DAT[7] src_BT_EEPROM_CFG	2V8	LCD_D7	DISP0_DAT7	**DISP0_DAT[7]** CSPI_RDY USBOH3_USBH2_DATA[7] USBPHY2_VBUSVALID	GPIO4[28]	LCD Data Bus
125	3V3	GPIO	A13	3V3	LCD_D8/GPIO	A13	1V8	LCD_D8	IMX_DISP1_DAT8	ipu_DISP1_DAT[8] src_BT_SRC[0]	2V8	LCD_D8	DISP0_DAT8	**DISP0_DAT[8]** PWM1_PWMO WDOG1_WDOG_B USBPHY2_AVALID	GPIO4[29]	
126	3V3	GPIO	A15	3V3	LCD_D9/GPIO	A15	1V8	LCD_D9	IMX_DISP1_DAT9	ipu_DISP1_DAT[9] src_BT_SRC[1]	2V8	LCD_D9	DISP0_DAT9	**DISP0_DAT[9]** PWM2_PWMO WDOG2_WDOG_B USBPHY2_VSTATUS[0]	GPIO4[30]	
127	3V3	LD6	LD6	3V3	LCD_D10	DISP1_DAT6	1V8	LCD_D10	IMX_DISP1_DAT10	ipu_DISP1_DAT[10] src_BT_SPARE_SIZE	2V8	LCD_D10	DISP0_DAT10	**DISP0_DAT[10]** USBOH3_USBH2_STP USBPHY2_VSTATUS[1]	GPIO4[31]	LCD Data Bus
128	3V3	LD7	LD7	3V3	LCD_D11	DISP1_DAT7	1V8	LCD_D11	IMX_DISP1_DAT11	ipu_DISP1_DAT[11] src_BT_LPB_FREQ[2]	2V8	LCD_D11	DISP0_DAT11	**DISP0_DAT[11]** USBOH3_USBH2_NXT USBPHY2_VSTATUS[2]	GPIO5[5]	LCD Data Bus
129	GND	GND		GND	GND		GND	GND	GND		GND	GND				

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
130	3V3	LD8	LD8	3V3	LCD_D12	DISP1_DAT8	1V8	LCD_D12	IMX_DISP1_DAT12	ipu_DISP1_DAT[12] src_MLC_SEL	2V8	LCD_D12	DISP0_DAT12	**DISP0_DAT[12]** USBOH3_USBH2_CLK USBPHY2_VSTATUS[3]	GPIO5[6]	LCD Data Bus
131	3V3	LD9	LD9	3V3	LCD_D13	DISP1_DAT9	1V8	LCD_D13	IMX_DISP1_DAT13	ipu_DISP1_DAT[13] src_BT_MEM_CTL[0]	2V8	LCD_D13	DISP0_DAT13	**DISP0_DAT[13]** AUD5_RXFS USBPHY2_VSTATUS[4]	GPIO5[7]	LCD Data Bus
132	3V3	LD10	LD10	3V3	LCD_D14	DISP1_DAT10	1V8	LCD_D14	IMX_DISP1_DAT14	ipu_DISP1_DAT[14] src_BT_MEM_CTL[1]	2V8	LCD_D14	DISP0_DAT14	**DISP0_DAT[14]** AUD5_RXC USBPHY2_VSTATUS[5]	GPIO5[8]	LCD Data Bus
133	3V3	LD11	LD11	3V3	LCD_D15	DISP1_DAT11	1V8	LCD_D15	IMX_DISP1_DAT15	ipu_DISP1_DAT[15] src_BT_BUS_WIDTH	2V8	LCD_D15	DISP0_DAT15	**DISP0_DAT[15]** ECSPI1_SS1 ECSPI2_SS1 USBPHY2_VSTATUS[6]	GPIO5[9]	LCD Data Bus
134	3V3	GPIO	A16	3V3	LCD_D16/GPIO	A16	1V8	LCD_D16	IMX_DISP1_DAT16	ipu_DISP1_DAT[16] src_BT_PAGE_SIZE[0]	2V8	LCD_D16	DISP0_DAT16	**DISP0_DAT[16]** ECSPI2_MOSI AUD5_TXC SDMA_EXT_EVENT[0] USBPHY2_VSTATUS[7]	GPIO5[10]	Signal for common electrode driving signal preparation (Sharp panel dedicated signal).
135	3V3	GPIO	A14	3V3	LCD_D17/GPIO	A14	1V8	LCD_D17	IMX_DISP1_DAT17	ipu_DISP1_DAT[17] src_BT_PAGE_SIZE[1]	2V8	LCD_D17	DISP0_DAT17	**DISP0_DAT[17]** ECSPI2_MISO AUD5_TXD SDMA_EXT_EVENT[1]	GPIO5[11]	Sampling start signal for left and right scanning.
136	3V3	LD12	LD12	3V3	LCD_D18	DISP1_DAT12	1V8	LCD_D18	IMX_DISP1_DAT18	ipu_DISP1_DAT[18] src_BT_WEIM_MUXED[0]	2V8	LCD_D18	DISP0_DAT18	**DISP0_DAT[18]** ECSPI2_SS0 AUD5_TXFS AUD4_RXFS WEIM_CS[2]	GPIO5[12]	LCD Data Bus
137	3V3	LD13	LD13	3V3	LCD_D19	DISP1_DAT13	1V8	LCD_D19	IMX_DISP1_DAT19	ipu_DISP1_DAT[19] src_BT_WEIM_MUXED[1]	2V8	LCD_D19	DISP0_DAT19	**DISP0_DAT[19]** ECSPI2_SCLK AUD5_RXD AUD4_RXC WEIM_CS[3]	GPIO5[13]	LCD Data Bus
138	3V3	LD14	LD14	3V3	LCD_D20	DISP1_DAT14	1V8	LCD_D20	IMX_DISP1_DAT20	ipu_DISP1_DAT[20] src_BT_MEM_TYPE[0]	2V8	LCD_D20	DISP0_DAT20	**DISP0_DAT[20]** ECSPI1_SCLK AUD4_TXC	GPIO5[14]	LCD Data Bus
139	3V3	LD15	LD15	3V3	LCD_D21	DISP1_DAT15	1V8	LCD_D21	IMX_DISP1_DAT21	ipu_DISP1_DAT[21] src_BT_MEM_TYPE[1]	2V8	LCD_D21	DISP0_DAT21	**DISP0_DAT[21]** ECSPI1_MOSI AUD4_TXD	GPIO5[15]	LCD Data Bus
140	3V3	LD16 / GPIO	D15	3V3	LCD_D22/GPIO	D15	1V8	LCD_D22	IMX_DISP1_DAT22	ipu_DISP1_DAT[22] src_BT_LPB_FREQ[0]	2V8	LCD_D22	DISP0_DAT22	**DISP0_DAT[22]** ECSPI1_MISO AUD4_TXFS	GPIO5[16]	LCD Data Bus
141	3V3	LD17 / GPIO	D14	3V3	LCD_D23/GPIO	D14	1V8	LCD_D23	IMX_DISP1_DAT23	src_BT_LPB_FREQ[0] src_BT_LPB_FREQ[1]	2V8	LCD_D23	DISP0_DAT23	**DISP0_DAT[23]** ECSPI1_SS0 AUD4_RXD	GPIO5[17]	LCD Data Bus
142	GND	GND		GND	GND		GND	GND	GND		GND	GND				
143	3V3	HSYNC	HSYNC	3V3	HSYNC	HSYNC	1V8	LCD_HSYNC	IMX_DI1_PIN2	ipu_DI1_PIN2	2V8	LCD_HSYNC	DI0_PIN2	**DI0_PIN2** AUD6_TXD USBPHY1_ENDSESSION	GPIO4[18]	Line Pulse or HSync
144	3V3	VSYNC	VSYNC	3V3	VSYNC	VSYNC	1V8	LCD_VSYNC	IMX_DI1_PIN3	ipu_DI1_PIN3	2V8	LCD_VSYNC	DI0_PIN3	**DI0_PIN3** AUD6_TXFS USBPHY1_IDDIG	GPIO4[19]	Frame Sync or Vsync—This signal also serves as the clock signal output for gate; driver (dedicated signal SPS for Sharp panel HRTFT)
145	3V3	OE_ACD	OE_ACD	3V3	OE_ACD	OE_ACD	1V8	LCD_OE_ACD	IMX_DI1_PIN15	ipu_DI1_PIN15	2V8	LCD_OE_ACD	DI0_PIN15	**DI0_PIN15** AUD6_TXC USBPHY1_BVALID	GPIO4[17]	Alternate Crystal Direction/Output Enable
146	3V3	LSCLK	LSCLK	3V3	LSCLK	LSCLK	1V8	LCD_LSCLK	IMX_DI1_DISP_CLK	ipu_DI1_DISP_CLK	2V8	LCD_SCLK	DI0_DISP_CLK	**DI0_DISP_CLK** USBOH3_USBH2_DIR USBPHY1_AVALID	GPIO4[16]	Shift Clock
147	GND	GND		GND	GND		GND	GND	GND		GND	GND				
	3V3	GPIO	A10	3V3	GPIO	A10	1V8	GPIO0	IMX_GPIO1_0	esdhc1_CD	3V3	CSI1_MCLK	NANDF_CS2	NANDF_CS[2]	GPIO6[15]	

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
148										gpio1_GPIO[0] cspi_SS1				SISG[0] / ESAI1_TX0 WEIM_CRE CCM_CSI0_MCLK MLBSIG USBPHY1_VSTATUS[6]		
149	3V3	GPIO	A17	3V3	GPIO	A17	1V8	GPIO1	IMX_GPIO1_1	esdhc1_WP gpio1_GPIO[1] cspi_MISO	3V3	CSI1_PIXCLK	EIM_A16	esdhc1_WPWEIM_A[16] DI1_DISP_CLK CSI1_PIXCLK BT_CFG1[1]	GPIO2[22]	
150	3V3	GPIO	A18	3V3	GPIO	A18	1V8	GPIO2	IMX_EIM_D20	gpio2_GPIO[4] audmux_AUD4_TXD	3V3	CSI1_VSYNC	EIM_D29	gpio2_GPIO[4]WEIM_D[29] UART2_RTS DISPB0_SER_RS CSPI_SS0 / DI1_PIN15 CSI1_VSYNC DI0_PIN14	GPIO3[29]	
151	3V3	GPIO	A19	3V3	GPIO	A19	1V8	GPIO3	IMX_EIM_D21	gpio2_GPIO[5] audmux_AUD4_RXD	3V3	CSI1_HSYNC	EIM_EB3	WEIM_EB[3] UART3_RTS UART1_RI / DI1_PIN3 CSI1_HSYNC DI1_PIN16	GPIO2[31]	
152	3V3	GPIO	A20	3V3	GPIO	A20	1V8	GPIO4	IMX_EIM_D22	gpio2_GPIO[6] audmux_AUD4_TXC	3V3	CSI1_D12	EIM_A17	WEIM_A[17] DISP1_DAT[12] CSI1_D[12] BT_CFG1[2]	GPIO2[21]	
153	3V3	GPIO	A21	3V3	GPIO	A21	1V8	GPIO5	IMX_EIM_D23	gpio2_GPIO[7] spdif_OUT1 audmux_AUD4_TXFS	3V3	CSI1_D13	EIM_A18	WEIM_A[18] DISP1_DAT[13] CSI1_D[13] BT_CFG1[3]	GPIO2[20]	
154	3V3	GPIO	A22	3V3	GPIO	A22	1V8	GPIO6	IMX_DI1_PIN11	gpio3_GPIO[0] ecspi1_SS2	3V3	CSI1_D14	EIM_A19	WEIM_A[19] DISP1_DAT[14] CSI1_D[14] BT_CFG1[4]	GPIO2[19]	
155	3V3	GPIO	A23	3V3	GPIO	A23	1V8	GPIO7	IMX_DI1_PIN12	gpio3_GPIO[1]	3V3	CSI1_D15	EIM_A20	WEIM_A[20] DISP1_DAT[15] CSI1_D[15] BT_CFG1[5]	GPIO2[18]	
156	3V3	GPIO	A24	3V3	GPIO	A24	1V8	GPIO8	IMX_DISPB2_SER_DIN	gpio3_GPIO[5]	3V3	CSI1_D16	EIM_A21	WEIM_A[21] DISP1_DAT[16] CSI1_D[16] BT_CFG1[6]	GPIO2[17]	
157	3V3	GPIO	A25	3V3	GPIO	A25	1V8	GPIO9	IMX_DISPB2_SER_DIO	gpio3_GPIO[6] wdog1_WDOG_RST_B_DEB	3V3	CSI1_D17	EIM_A22	WEIM_A[22] DISP1_DAT[17] CSI1_D[17] BT_CFG1[7]	GPIO2[16]	
158	3V3	GPIO	CS0	3V3	GPIO	CS0	1V8	GPIO10	IMX_DISPB2_SER_CLK	gpio3_GPIO[7] wdog2_WDOG_RST_B_DEB	3V3	CSI1_D18	EIM_A23	WEIM_A[23] DISP1_DAT[18] CSI1_D[18] / SISG[3] USBPHY2_ENDSESSION	GPIO6[6]	
159	3V3	GPIO	CS1	3V3	GPIO	CS1	1V8	GPIO11	IMX_DISPB2_SER_RS	gpio3_GPIO[8]	3V3	CSI1_D19	EIM_A24	WEIM_A[24] DISP1_DAT[19] CSI1_D[19] / SISG[2] USBPHY2_BVALID	GPIO5[4]	
160	GND	GND		GND	GND		GND	GND	GND		GND	GND				
161	3V3	D[0]	D[0]	3V3	D[0]	NANDF_D[0]	1V8	EMI-CS0	MIX_EIM_CS0	emi_EIM_CS0 gio2_GPIO[25]	3V3		CSI0_DAT8	CSI0_D[8] / KPP_COL[7] ECSPI2_SCLK USBOH3_USBH3_OC I2C1_SDA	GPIO5[26]	
162	3V3	D[1]	D[1]	3V3	D[1]	NANDF_D[1]	1V8	EMI-CS1	MIX_EIM_CS1	emi_EIM_CS1 gpio2_GPIO[26]	3V3		CSI0_DAT9	CSI0_D[9] / KPP_ROW[7] ECSPI2_MOSI USBOH3_USBH3_PWR I2C1_SCL	GPIO5[27]	
163	3V3	D[2]	D[2]	3V3	D[2]	NANDF_D[2]	1V8	EMI-CS4	MIX_EIM_DTACK	emi_WEIM_DTACK_B gpio2_GPIO[31] emi_EIM_A[27]	3V3		CSI0_DAT10	CSI0_D[10] UART1_TXD ECSPI2_MISO AUD3_RXC	GPIO5[28]	

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
										gpio2_GPIO[21]						
164	3V3	D[3]	D[3]	3V3	D[3]	NANDF_D[3]	1V8	EMI_WAIT	MIX_EIM_WAIT	emi_EIM_WAIT	3V3		CSI0_DAT11	CSI0_D[11] UART1_RXD ECSPI2_SS0 AUD3_RXFS	GPIO5[29]	
165	3V3	D[4]	D[4]	3V3	D[4]	NANDF_D[4]	1V8	EMI_EB0	MIX_EIM_EB0	emi_EMI_EB[0]	3V3		EIM_D22	WEIM_D[22] DI0_PIN1 DISPB0_SER_DIN CSPI_MISO USBOH3_USBOTG_PWR	GPIO3[22]	
166	3V3	D[5]	D[5]	3V3	D[5]	NANDF_D[5]	1V8	EMI_EB1	MIX_EIM_EB1	emi_EIM_EB[1]	3V3		EIM_D23	WEIM_D[23] UART3_CTS UART1_DCD DI0_D0_CS DI1_PIN2 CSI1_DATA_EN DI1_PIN14	GPIO3[23]	
167	3V3	D[6]	D[6]	3V3	D[6]	NANDF_D[6]	1V8	EMI_OE	MIX_EIM_OE	emi_EIM_OE gpio2_GPIO[24]	3V3	CKIH1	CKIH1			
168	3V3	D[7]	D[7]	3V3	D[7]	NANDF_D[7]	1V8	EMI_LBA	MIX_EIM_LBA	emi_EIM_LBA gpio3_GPIO[1]	3V3	TVDAC_IOB	TVDAC_IOB			
169	3V3	A[0]	A[0]	3V3	A[0]	A[0]	1V8	EMI_RW	MIX_EIM_RW	emi_EIM_RW	3V3	TVDAC_IOG	TVDAC_IOG			
170	3V3	A[1]	A[1]	3V3	A[1]	A[1]	1V8	EMI_BCLK	MIX_EIM_BCLK	emi_EIM_BCLK	3V3	TVDAC_IOR	TVDAC_IOR			
171	GND	GND		GND	GND		GND	GND	GND		GND	GND				
172	NDY	TAMPER_A	TAMPER_A	NDY	TAMPER_A	TAMPER_A	1V8	EMI_A0	MIX_EIM_A16	emi_EIM_A[16] gpio2_GPIO[10] src_OSC_FREQ_SEL[0]	3V3		GPIO_13		GPIO4[3]	DRYICE external tamper detect pins, active high. If either TAMPER_A or TAMPER_B asserted, then external tampering is detected. Should be tied to pull-down if no tamper detection is required on board.
173	NDY	TAMPER_B	TAMPER_B	NDY	TAMPER_B	TAMPER_B	1V8	EMI_A1	MIX_EIM_A17	emi_EIM_A[17] gpio2_GPIO[11] src_OSC_FREQ_SEL[1]	3V3	EIM_CS0	EIM_CS0	WEIM_CS[0] ECSPI2_SCLK DI1_PIN5	GPIO2[23]	
174	NDY	MESH_C	MESH_C	NDY	MESH_C	MESH_C	1V8	EMI_A2	MIX_EIM_A18	emi_EIM_A[18] gpio2_GPIO[12] src_BT_LPB[0]	3V3	EIM_CS1	EIM_CS1	WEIM_CS[1] ECSPI2_MOSI DI1_PIN6	GPIO2[24]	Wire-mesh tamper detect pins which can be routed at the PCB board to detect attempted tampering of a protected wire. MESH_C is active high and should be connected to an on-board pull-down if no tamper detection is required. MESH_D is active low and should be connected to an on-board pull-up if no tamper detection is required.
175	NDY	MESH_D	MESH_D	NDY	MESH_D	MESH_D	1V8	EMI_A3	MIX_EIM_A19	emi_EIM_A[19] gpio2_GPIO[13] src_BT_LPB[1]	3V3	GPIO	CSI0_DATA_EN	CSI0_DATA_EN	GPIO5[20]	
176	3V3	A[2]	A[2]	3V3	A[2]	A[2]	1V8	EMI_A4	MIX_EIM_A20	emi_EIM_A[20] gpio2_GPIO[14] src_BT_UART_SRC[0]	3V3	EIM_WAIT	EIM_WAIT	WEIM_WAIT WEIM_DTACK_B	GPIO5[0]	
177	3V3	A[3]	A[3]	3V3	A[3]	A[3]	1V8	EMI_A5	MIX_EIM_A21	emi_EIM_A[21] gpio2_GPIO[15] src_BT_UART_SRC[1]	3V3	EIM_EB0	EIM_EB0	WEIM_EB[0] DISP1_DAT[11] CSI1_D[11] GPC_PMIC_RDY BT_CFG2[7]	GPIO2[28]	
178	3V3	A[4]	A[4]	3V3	A[4]	A[4]	1V8	EMI_A6	MIX_EIM_A22	emi_EIM_A[22] gpio2_GPIO[16]	3V3	EIM_EB1	EIM_EB1	WEIM_EB[1] DISP1_DAT[10] CSI1_D[10] BT_CFG2[6]	GPIO2[29]	

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
179	3V3	A[5]	A[5]	3V3	A[5]	A[5]	1V8	EMI_A7	MIX_EIM_A23	emi_EIM_A[23] gpio2_GPIO[17] src_BT_HPN_EN	3V3	EIM_OE	EIM_OE	WEIM_OE ECSPI2_MISO DI1_PIN7 USBPHY2_IDDIG	GPIO2[25]	
180	3V3	A[6]	A[6]	3V3	A[6]	A[6]	1V8	EMI_A8	MIX_EIM_A24	emi_EIM_A[24] gpio2_GPIO[18]	3V3	EIM_LBA	EIM_LBA	WEIM_LBA ECSPI2_SS1 DI1_PIN17 BT_CFG1[0]	GPIO2[27]	
181	3V3	A[7]	A[7]	3V3	A[7]	A[7]	1V8	EMI_A9	MIX_EIM_A25	emi_EIM_A[25] gpio2_GPIO[19]	3V3	EIM_RW	EIM_RW	WEIM_RW ECSPI2_SS0 DI1_PIN8 USBPHY2_ HOSTDISCONNECT	GPIO2[26]	
182	3V3	A[8]	A[8]	3V3	A[8]	A[8]	1V8	EMI_A10	MIX_EIM_A26	emi_EIM_A[26] gpio2_GPIO[20]	3V3	EIM_BCLK	EIM_BCLK	WEIM_BCLK		
183	GND	GND		GND	GND		GND	GND	GND		GND	GND				
184	ANG	REF	REF	ANG	REF	REF	1V8	EMI_D0	MIX_EIM_DA0	emi_EIM_DA[0]	3V3	EIM_DA0	EIM_DA0	NAND_WEIM_DA[0] DISP1_DAT[9] CSI1_D[9] / BT_CFG2[5]	GPIO3[0]	Touchscreen ADC External reference voltage (2.5 V). REF may be left floating if the internally generated 2.5 V supply is enabled. Use of an external reference is recommended.
185	ANG	XN	XN	ANG	XN	XN	1V8	EMI_D1	MIX_EIM_DA1	emi_EIM_DA[1]	3V3	EIM_DA1	EIM_DA1	NAND_WEIM_DA[1] DISP1_DAT[8] CSI1_D[8] / BT_CFG2[4]	GPIO3[1]	Touchscreen ADC input channels
186	ANG	XP	XP	ANG	XP	XP	1V8	EMI_D2	MIX_EIM_DA2	emi_EIM_DA[2]	3V3	EIM_DA2	EIM_DA2	NAND_WEIM_DA[2] DISP1_DAT[7] CSI1_D[7] / BT_CFG2[3]	GPIO3[2]	
187	ANG	YN	YN	ANG	YN	YN	1V8	EMI_D3	MIX_EIM_DA3	emi_EIM_DA[3]	3V3	EIM_DA3	EIM_DA3	NAND_WEIM_DA[3] DISP1_DAT[6] CSI1_D[6] / BT_CFG2[2]	GPIO3[3]	
188	ANG	YP	YP	ANG	YP	YP	1V8	EMI_D4	MIX_EIM_DA4	emi_EIM_DA[4]	3V3	EIM_DA4	EIM_DA4	NAND_WEIM_DA[4] DISP1_DAT[5] CSI1_D[5] / BT_CFG3[7]	GPIO3[4]	
189	ANG	WIPER	WIPER	ANG	WIPER	WIPER	1V8	EMI_D5	MIX_EIM_DA5	emi_EIM_DA[5]	3V3	EIM_DA5	EIM_DA5	NAND_WEIM_DA[5] DISP1_DAT[4] CSI1_D[4] / BT_CFG3[6]	GPIO3[5]	
190	ANG	INAUX0	INAUX0	ANG	INAUX0	INAUX0	1V8	EMI_D6	MIX_EIM_DA6	emi_EIM_DA[6]	3V3	EIM_DA6	EIM_DA6	NAND_WEIM_DA[6] DISP1_DAT[3] CSI1_D[3] / BT_CFG3[5]	GPIO3[6]	General purpose measurements channels
191	ANG	INAUX1	INAUX1	ANG	INAUX1	INAUX1	1V8	EMI_D7	MIX_EIM_DA7	emi_EIM_DA[7]	3V3	EIM_DA7	EIM_DA7	NAND_WEIM_DA[7] DISP1_DAT[2] CSI1_D[2] / BT_CFG3[4]	GPIO3[7]	General purpose measurements channels
192	ANG	INAUX2	INAUX2	ANG	INAUX2	INAUX2	1V8	EMI_D8	MIX_EIM_DA8	emi_EIM_DA[8]	3V3	EIM_DA8	EIM_DA8	NAND_WEIM_DA[8] DISP1_DAT[1] CSI1_D[1] / BT_CFG3[3]	GPIO3[8]	
193	PWR	NVCC_DRYICE	NVCC_DRYICE	PWR	NVCC_DRYICE	NVCC_DRYICE	1V8	EMI_D9	MIX_EIM_DA9	emi_EIM_DA[9]	3V3	EIM_DA9	EIM_DA9	NAND_WEIM_DA[9] DISP1_DAT[0] CSI1_D[0] / BT_CFG3[2]	GPIO3[9]	DRYICE power supply output. Source can be SoC supply or backup supply. This pin can be used to power external tamper detect components.
194	3V3	A [9]	A [9]	3V3	A [9]	A [9]	1V8	EMI D10	MIX_EIM_DA10	emi_EIM_DA[10]	3V3	EIM_DA10	EIM_DA10	NAND_WEIM_DA[10] DI1_PIN15 CSI1_DATA_EN BT_CFG3[1]	GPIO3[10]	
195	3V3	A [11]	A [11]	3V3	A [11]	A [11]	1V8	EMI_D11	MIX_EIM_DA11	emi_EIM_DA[11]	3V3	EIM_DA11	EIM_DA11	NAND_WEIM_DA[11] DI1_PIN2 / CSI1_HSYNC	GPIO3[11]	
196	3V3	A [12]	A [12]	3V3	A [12]	A [12]	1V8	EMI_D12	MIX_EIM_DA12	emi_EIM_DA[12]	3V3	EIM_DA12	EIM_DA12	NAND_WEIM_DA[12] DI1_PIN3 CSI1_VSYNC	GPIO3[12]	

Pin	Type	Function BASEBOARD	Pin Name BASEBOARD	Type	Function i.MX25	Pin Name i.MX25	Type	Baseboard	Module	Functions	Type	Function	i.MX53 Pad Name	Alternate Functions	GPIO	Description
197	NC			NC			1V8	EMI_D13	MIX_EIM_DA13	emi_EIM_DA[13]	3V3	EIM_DA13	EIM_DA13	NAND_WEIM_DA[13] DI1_D0_CS CCM_DI1_EXT_CLK	GPIO3[13]	Not Connected
198	NC			NC			1V8	EMI_D14	MIX_EIM_DA14	emi_EIM_DA[14]	3V3	EIM_DA14	EIM_DA14	NAND_WEIM_DA[14] DI1_D1_CS CCM_DI0_EXT_CLK	GPIO3[14]	Not Connected
199	NC			NC			1V8	EMI_D15	MIX_EIM_DA15	emi_EIM_DA[15]	3V3	EIM_DA15	EIM_DA15	NAND_WEIM_DA[15] DI1_PIN1 DI1_PIN4	GPIO3[15]	Not Connected
200	GND	GND		GND	GND		GND	GND								