

Sleep mode - SDRAM self refresh problem

(only for DIMM PC v.1.1/23/03 - see bottom layer of DIMM module):

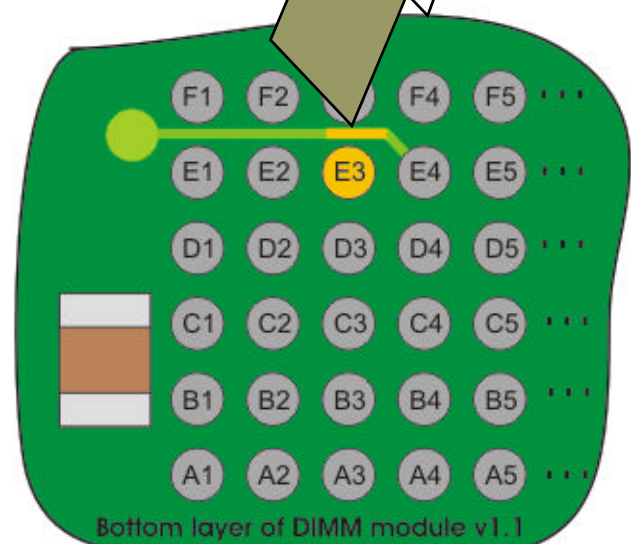
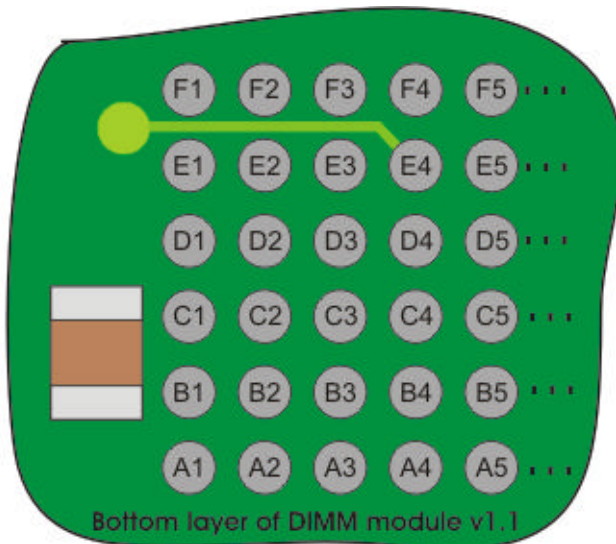
Problem: System crashes after sleep mode

Solution : Change signal **SDCKE1**(E3 pin on PXA255) instead **SDCKE0**(E4 pin on PXA255)

Fix the problem:

1. Find E4 pin on bottom layer of DIMM module
2. With scalpel (or sharp knife) remove green mask on line from E4
3. With scalpel (or sharp knife) remove green mask from E3 pin
4. Use hot soldering station with sharp point for line break. If you are using scalpel, **be very careful** - there are other lines in next layers.
5. Make short circuit between E3 pin and line (see picture)
6. Check connections

!!! ONLY on your OWN RISK!!!



Not Short Circuit!!!
Short Circuit

Not Short Circuit!!!

